

Attorney's Docket No.: 10559-855001
Client's Ref. No.: P17303

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Applicant : Valery M. Dubin
Serial No. : 10/671,322
Filed : September 24, 2003

Art Unit : 2815
Examiner : Bradley Baumeister

Title : Nanotubes for Integrated Circuits

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attached to this facsimile communication cover sheet is Response to Restriction Requirement, faxed this 28th day of July, 2004, to the United States Patent and Trademark Office.

Date: July 28, 2004

Respectfully submitted,



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Attorney's Docket No.: 10559/855001 / P17303/Intel Corporation

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Art Unit : 2815

Serial No. : 10/671,322

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Assignee : Intel Corporation

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Title : NANOTUBES FOR INTEGRATED CIRCUITS

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RESPONSE TO RESTRICTION REQUIREMENT

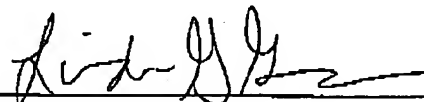
In Response to the Notice of Restriction dated June 29, 2004, in the above-referenced case, applicant herewith elects group I, claims 1-4, without traverse. Please also examine linking claim 14, per MPEP 809.

Kindly retain the non-elected claims as withdrawn from consideration. However, should all other claims be allowable, applicant will consent to cancellation of the non-elected claims in groups II, III, and V by examiner's amendment (see MPEP 821.02).

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 07/28/04



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